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| circuit detecting "delay time can be adjusted" | Search |

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P Demeyer

H Iwamoto

Y Murai

T Miyatani

C Ikoh

Did you mean: circuit *detection* "delay time can be adjusted"

Circuit breaker with digital solid-state trip unit fitted with a calibration circuit - all 4 versions »
P Demeyer - US Patent 4,682,264, 1987 - Google Patents
... The output SI is connected to long **delay time can be adjusted** by the switch 78 which
the relay 14 to control tripping of the **circuit** breaker 10, ...
Cited by 88 - Related Articles - Web Search

**Internal clock signal generation circuit including delay line, and synchronous type
semiconductor ...** - all 3 versions »
H Iwamoto, Y Murai - US Patent 5,946,268, 1999 - Google Patents
... [54] INTERNAL CLOCK SIGNAL GENERATION **CIRCUIT** INCLUDING DELAY LINE, AND SYNCHRONOUS
TYPE SEMICONDUCTOR MEMORY DEVICE INCLUDING INTERNAL CLOCK SIGNAL ...
Cited by 18 - Related Articles - Web Search

Delay time control circuit - all 3 versions »
T Sekino - US Patent 5,869,992, 1999 - Google Patents
... as mentioned above, a **circuit** for **detecting** the number ... time of a CMOS gate delay
circuit with high ... CMOS gate so that the **delay time can be adjusted** with high ...
Cited by 6 - Related Articles - Web Search

Delay circuit for electric blasting, detonating primer having delay circuit and system for ... -
all 2 versions »
K Ochi, M Harada - US Patent 4,825,765, 1989 - Google Patents
... The voltage **detecting circuit** 34 for **detecting** the voltage ... 47 in the voltage depicting
circuit 34, the ... SWi, SW 2 . . . SW n , the **delay time can be adjusted**. ...
Cited by 8 - Related Articles - Web Search

Illuminating apparatus for a microscope - all 2 versions »
C Ikoh, M Osaki - US Patent 5,517,353, 1996 - Google Patents
... 1, illustrating a revolver position **detecting** switch in the **circuit** of FIG. 1; FIG. ...
5, illustrating a revolver position **detecting** switch in the **circuit** of FIG. ...
Cited by 10 - Related Articles - Web Search

**Photon-counting device compatible with conventional flow cytometric data acquisition
electronics** - all 3 versions »
A Agronskaia, A Florians, KO van der Werf, JM ... - Cytometry, 1998 - doi.wiley.com
... and analyzed further to **detect** photon bursts. ... **Circuit** Description A practical
realization of the above ... The **delay time can be adjusted** (to match the transit time ...
Cited by 8 - Related Articles - Web Search

Apparatus for actuating the safety devices for vehicle occupants - all 2 versions »
S Kitao, A Kimura, Y Ootani, I Hitomi, A Kuroiwa - US Patent 5,668,528, 1997 - Google Patents
... comprised. The passenger detector is used for **detecting** a ... 101 "^ *f switch 102
^T^^^L * **circuit** is broken at one squib. ... The **delay time can be adjusted** by ...
Cited by 9 - Related Articles - Web Search

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P Demeyer

H Iwamoto

Y Murai

T Miyatani

C Ikoh

Flow responsive time delay pump motor cut-off logic - all 2 versions »
GL Rasmuson, P Pigue... - US Patent 5,819,848, 1998 - Google Patents
... operation of predetermined duration following **detection** of an ... The outer **circuit** board
52 is also ... The **delay time can be adjusted** between one second and to a ...
Cited by 3 - Related Articles - Web Search

Semiconductor memory device and method of controlling the same - all 4 versions »
A Sako - US Patent 6,741,514, 2004 - Google Patents
... a fuse **circuit** 45 is connected to the variable delay elements 42 and 43. By providing
the variable delay elements 42 and 43, the 35 **delay time can be adjusted**, ...
Cited by 3 - Related Articles - Web Search

Signal delaying outputting **circuit** - all 3 versions »
T Tachiyama - US Patent 5,523,711, 1996 - Google Patents
... Further, the value of the voltage **delay time can be adjusted** arbitrarily by
individually ... which is consti -tuted from the temperature **detection circuit** 21 and ...
Cited by 2 - Related Articles - Web Search

Semiconductor memory device allowing mounting of built-in self test **circuit** without addition
of ... - all 4 versions »
T Tanizaki, T Hamamoto - US Patent 6,782,498, 2004 - Google Patents
... FIG. 4 ZI NTAD D< 1 2 :0> EXT.BA<1 :0> ZINTBA<1 :0> HIGH POT ENTIAL **DETECTION CIRCUIT**
1 92 TEST MODE DECODER TMBE TEST MODE SIGNALS 94 INTMRS 90 Page 5. ...
Cited by 2 - Related Articles - Web Search

Slew rate adjusting **circuit** and semiconductor device - all 4 versions »
M Shimoda - US Patent 6,518,808, 2003 - Google Patents
... Thus, the **detection** result data Pmr and Nmr are interrelated ... actual drive capability
of the output **circuit**, and the **delay time can be adjusted** only within ...
Cited by 2 - Related Articles - Web Search

Magnetic digital storage method and system wherein magnetic transition points in adjacent
recorded ... - all 3 versions »
M Umemoto, Y Shiroishi, R Tsuchiya, K Akagi, T ... - US Patent 5,786,952, 1998 - Google Patents
... the conventional technique, there is used a **detecting** ... 16 and playback equalization
circuit 17. ... the **delay time can be adjusted** by considering the manner similar ...
Cited by 2 - Related Articles - Web Search

Delay time control **circuit** - all 5 versions »
T Okayasu, T Sekino - US Patent 6,462,598, 2002 - Google Patents
... such as a **circuit** for **detecting** the 25 ... of a semi -conductor gate delay **circuit** with
high ... semiconductor gate so that the **delay time can be adjusted** with high ...
Cited by 1 - Related Articles - Web Search

Logic **circuit** having phase-controlled data receiving interface - all 4 versions »
T Saito - US Patent 6,407,583, 2002 - Google Patents
... The receiving interlace includes adetecting **circuit** lor **detecting** whether or not

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detecting abnormal operation memory coincid   **Search**

Scholar Results **1 - 1** of **1** for <u>**detecting abnormal operation memory coincident**</u> OR noncoincidence "<u>delay time can</u> be ad<u></u>

Tip: Try removing quotes from your search to get more results.

... for **detecting abnormal operation** of **memory** and integrated circuit and method for detectiing **abnormal**
....
A Yusa - 2004 - freepatentsonline.com
... The explanation of the flow of **operation** from the ... D-FF 207 becomes reset, and the
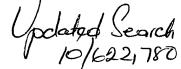abnormality **detecting** signal 230 ... of access speed to be judged **abnormal** can be ...
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Fri, 1 Feb 2008, 9:49:27 AM EST

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- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

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		Results
#1	(((detect abnormal)<in>metadata) <and> ((comparison) <in>metadata))<and> ((delay time adjusted)<in>metadata)	0
#2	(((delay circuit)<in>metadata) <and> ((comparison) <in>metadata))<and> ((adjustabe time)<in>metadata)	0
#3	(((detect abnormal)<in>metadata) <and> ((dalay time) <in>metadata))<and> ((adjustable)<in>metadata)	0
#4	(((memory)<in>metadata) <and> ((delay time)<in>metadata)) <and> ((adjusted)<in>metadata)	0
#5	(((delay)<in>metadata) <and> ((coincident signal) <in>metadata))<and> ((adjustable)<in>metadata)	0

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Interference Search
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1072	(714/30).ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:49
L2	630	(714/719).ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:49
L3	2120	(714/724).ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:49
L4	1115	(714/733).ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:49
L5	443	(714/734).ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:50
L6	318	(714/735).ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:50
L7	783	(714/736).ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:50
L8	113587	(adjust$3 vary$4 modif$3) same delay$3 same time	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:51
L9	407073	(monitor$3 track$3 detect$3 determin$4) near5 (fault$3 error$3 abnormal fail$3 malfunction defect$3)	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:53
L10	1289	delay$3 with (predermin$3 prset$3 predefin$3) with time	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:55
L11	1088	(compar$3 match$3) with (incoincidence coincident) with output	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:55
L12	3018	8 same 9	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:56
L13	5	12 same 10	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:56
L14	0	12 same 11	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:56
L15	1	12 and 10 and 11	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:57

EAST Search History

L16	0	8 and 9 and 10 and 11 and 1	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:58
L17	0	8 and 9 and 10 and 11 and 2	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:58
L18	0	8 and 9 and 10 and 11 and 3	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:58
L19	0	8 and 9 and 10 and 11 and 4	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:58
L20	0	8 and 9 and 10 and 11 and 5	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:58
L21	0	8 and 9 and 10 and 11 and 6	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:58
L22	0	8 and 9 and 10 and 11 and 7	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 07:58